

Amendments to the Claims:

A listing of the entire set of pending claims (including amendments to the claims, if any) is submitted herewith per 37 CFR 1.121. This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently amended) An electroluminescent (EL) display device comprising an array of display pixels ~~(1)~~, each display pixel comprising an EL display element ~~(2)~~ and a current source circuit ~~(20)~~ for driving a current through the EL display element in dependence on a data voltage, wherein the display device is operable in at least first and second phases ~~(30,32)~~ within each frame period:

the first phase ~~(30)~~ having a first duration and during which a first one of a first plurality ~~(34)~~ of ~~analogue~~ drive currents can be driven through the EL display element; and

the second phase ~~(32)~~ having a second duration, different to the first duration, and during which a second one of a second plurality ~~(33)~~ of ~~analogue~~ drive currents can be driven through the EL display element, wherein the first and second ones of the respective pluralities of ~~analogue~~ drive currents are independently selectable and at least one of the first and second pluralities of drive currents include more than two drive current levels.

2. (Currently amended) A device as claimed in claim 1, wherein the first plurality of ~~analogue~~ drive currents comprises a number n of drive current levels including zero, and wherein the duration of one phase is approximately n times the duration of the other phase.

3. (Original) A device as claimed in claim 2, wherein n is 8.

4. (Currently amended) A device as claimed in ~~any preceding claim 1~~, wherein the first plurality ~~(31)~~ of ~~analogue~~ drive currents is the same as the second plurality ~~(33)~~ of ~~analogue~~ drive currents.

5. (Currently amended) A device as claimed in any one of claims 1 to 3, wherein the first plurality ~~(31)~~ of ~~analogue~~ drive currents comprises a first number n of drive current levels for providing the lowest n non-zero brightness levels, and the second plurality of ~~analogue~~ drive currents comprises a second number m of non-zero drive current levels ~~(34)~~ for providing the highest m brightness levels, where $n+m$ is the total number of brightness levels.

6. (Currently amended) A device as claimed in ~~any preceding claim 1~~, wherein each pixel comprises a drive transistor ~~(22)~~, a storage capacitor ~~(24)~~ for storing a gate voltage of the drive transistor ~~(22)~~ and an address transistor ~~(16)~~ for switching a data voltage to the gate of the drive transistor ~~(22)~~ during an addressing phase.

7. (Currently amended) A portable electronic device (40) comprising a display device as claimed in ~~any preceding claim 1~~.

8. (Currently amended) A method of driving an electroluminescent (EL) display device comprising an array of display pixels, each display pixel comprising an EL display element-(2) and a current source circuit-(20) for driving a current through the EL display voltage in dependence on a data voltage, the method comprising:

in a first phase-(30) having a first duration, driving a first one of a first plurality (34) of ~~analogue~~ drive currents through the EL display element; and

in a second phase-(32) having a second duration, different to the first duration, driving a second one of a second plurality-(33) of ~~analogue~~ drive currents through the EL display element, wherein the first and second ones of the plurality of ~~analogue~~ drive currents are selected to provide a desired combined EL display element output, and at least one of the first and second pluralities of drive currents includes more than two drive current levels.

9. (Currently amended) A method as claimed in claim 8, wherein the plurality of ~~analogue drive-levels currents~~ comprises a number n of drive levels, and wherein the duration of one phase is approximately n times the duration of the other phase.

10. (Original) A method as claimed in claim 9, wherein n is 8.

11. (Currently amended) A method as claimed in any one of claims 8 to 10, wherein the first plurality-(34) of ~~analogue~~ drive currents is the same as the second plurality (33) of ~~analogue~~ drive currents.

12. (Currently amended) A method as claimed in any one of claims 8 to 10, wherein the first plurality-(34) of ~~analogue~~ drive currents comprises a first number n of non-zero drive current levels for providing the lowest n brightness levels excluding zero, and the second plurality of ~~analogue~~ drive currents comprises a second number m of non-zero drive current levels-(34) for providing the highest m brightness levels, where $n+m$ is the total number of non-zero brightness levels.

13. (New) A device as claimed in claim 2, wherein the first plurality of drive currents is the same as the second plurality of drive currents.

14. (New) A device as claimed in claim 2, wherein each pixel comprises a drive transistor, a storage capacitor for storing a gate voltage of the drive transistor and an address transistor for switching a data voltage to the gate of the drive transistor during an addressing phase.